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TRANSMITTAL FORM <i>(to be used for all correspondence during pendency of filed application)</i>		Application Number	09/748,098
		Filing Date	December 21, 2000
		First Named Inventor	Nicholas J. Kelsey
		Group Art Unit Number	2183
		Examiner Name	Charles A. Harkness
Total Number of Pages in This Submission	20	Attorney Docket Number	20880-05093

ENCLOSURES (check all that apply)	
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REMARKS:	

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Signature:		
Attorney/Reg. No.:	Hector J. Ribera, Reg. No. 54,397	Dated: Jan 18, 2005

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TOTAL AMOUNT OF PAYMENT | (\$) 775

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FEE CALCULATION (continued)

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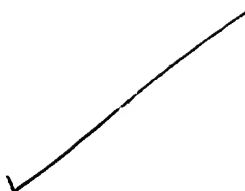
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Date _____

20880/05093/DOCS/1497069.1



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IN THE
UNITED STATES PATENT AND TRADEMARK OFFICE

APPLICANTS: Nicholas J. Kelsey, et al.
SERIAL NO.: 09/748,098
FILING DATE: December 21, 2000
TITLE: System and Method for Instruction Level Multithreading in an Embedded Processor Using Zero-Time Context Switching
EXAMINER: Charles A. Harkness
GROUP ART UNIT: 2183
ATTY. DKT. NO.: 20880-05093

CERTIFICATE OF MAILING

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Dated: Jan 19, 2005 By: Hector V. Ribera
Hector V. Ribera, Reg. No. 54,397

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AMENDMENT C AND RESPONSE

Sir:

This Amendment C and Response for the patent application identified above is in response to the Office Action dated October 19, 2004, which set a shortened statutory period for response that expires January 19, 2005.

Amendments to the Claims begin on page 2 of this paper.

Remarks begin on page 12 of this paper.

Kindly amend this application as indicated herein.

01/27/2005 HALI11 00000045 09748098

01 FC:2201 100.00 OP
02 FC:2202 675.00 OP

Case 20880-05093 (Amendment C and Response)

U.S. Serial No. 09/748,098